## ANALYSIS, DESIGN, SIMULATION AND IMPLEMENTATION OF DSP BASED EMBEDDED CONTROLLER FOR ACTIVE FILTER TO ELIMINATE AND COMPENSATE HARMONICS

A thesis submitted for the award of the

Degree of
DOCTOR OF PHILOSOPHY

in
Electrical Engineering
By

Mrs. Hina B Chandwani



ELECTRICAL ENGINEERING DEPARTMENT FACULTY OF TECHNOLOGY & ENGINEERING THE MAHARAJA SAYAJIRAO UNIVERSITY OF BARODA VADODARA – 390001 GUJARAT, INDIA

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