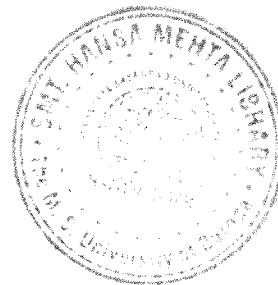


Certificate



This is to certify that the thesis entitled **‘Analysis, Design, Simulation and Implementation of DSP based Embedded Controller for Active Filter to eliminate and compensate harmonics’** submitted by **Mrs. Hina B Chandwani** in fulfillment of the degree of **DOCTOR OF PHILOSOPHY** in Electrical Engineering Department, Faculty of Technology & Engineering, The M. S. University of Baroda, Vadodara is a bonafide record of investigations carried out by her in the Department of Electrical Engineering, Faculty of Technology & Engineering, M. S. University of Baroda, Vadodara under my guidance and supervision. In my opinion the standards fulfilling the requirements of the Ph.D. Degree as prescribed in the regulations of the University has been attained

July 2007

A handwritten signature in black ink, appearing to read 'Satish K. Shah'.

Prof. Satish K. Shah

Department of Electrical Engineering,
Faculty of Technology & Engineering,
The Maharaja Sayajirao University of Baroda,
Vadodara – 390 001