

List of Tables

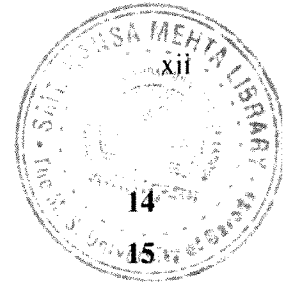


Table 2.1	Switching Combination in the α - β - γ Coordination	14
Table 2.2	Switching Variables used in the Space	15
Table 3.1	Circuit Constants	28
Table 3.2	Parameters of Line Simulator	40
Table 3.3	THD Control Method	45
Table 3.4	Balancing of the Compensating Currents(Case of Two APF)	48
Table 3.5	Parameters of the APF	49
Table 3.6	PWM VSI Switching Functions	54
Table 3.7	Inverter Switching Mode	55
Table 3.8	Switching Mode Combination for Larger Changes in Δi	55
Table 3.9	APF Control Parameters	57
Table 6.1	TOOLBOXES and Functional Blocks USED in the project	96
Table 8.1	TMS320C6711 DSK Peripheral Connector (J2): Pinouts	117
Table 8.2	TMS320C6711 DSK Memory Connector (J1): Pinouts	118
Table 8.3	Daughter Card Interface Features (DSK TMS320C6711)	119
Table 8.4	Flag Characteristics	129
Table 8.5	Fields in SIZE Structure	129