

CERTIFICATE



This is to certify that thesis titled “**COMPUTER AIDED DESIGN, SIMULATION AND DEVELOPMENT OF CIRCUIT-BREAKER SYNTHETIC TESTING CIRCUIT WITH AUTOMATIC CONTROLLER FOR CIRCUIT-BREAKERS**” submitted by **JITENDRA G. JAMNANI** for fulfilling requirements for the award of degree of “**DOCTOR OF PHILOSOPHY**” in Electrical Engineering is bonafide & authentic record based on computational programming investigations carried out by him in “**The DEPARTMENT OF ELECTRICAL ENGINEERING, FACULTY OF TECHNOLOGY AND ENGINEERING, THE MAHARAJA SAYAJIRAO UNIVERSITY OF BARODA, VADODARA** under my guidance and supervision. The work has satisfactorily attained the standard for fulfilling the requirements for the PhD degree as prescribed in the regulations of the university.

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Prof. (Mrs.) S. A. Kanitkar
(Research Guide)
Professor and Head
Electrical Engineering Department,
Faculty of Technology and Engineering
M. S. University, Baroda
Vadodara -390001

Prof. B. S. Parekh
Dean
Faculty of Technology and Engineering
M. S. University, Baroda
Vadodara -390001

Prof. (Mrs.) S. A. Kanitkar
Head
Electrical Engineering Deptt.
Faculty of Tech. & Engg
M. S. University of Baroda.
VADODARA - 390 001.