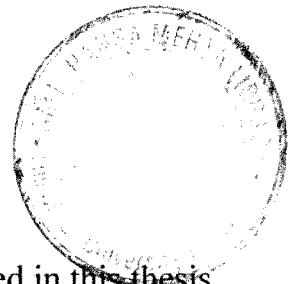


# DECLARATION



I, JITENDRA G. JAMNANI hereby declare that the work reported in this thesis titled **"COMPUTER AIDED DESIGN, SIMULATION AND DEVELOPMENT OF CIRCUIT-BREAKER SYNTHETIC TESTING CIRCUIT WITH AUTOMATIC CONTROLLER FOR CIRCUIT-BREAKERS"** submitted for the award of degree of

## **DOCTOR OF PHILOSOPHY IN ELECTRICAL ENGINEERING**

is original and was done by me in **THE DEPARTMENT OF ELECTRICAL ENGINEERING, FACULTY OF TECHNOLOGY AND ENGINEERING, THE MAHARAJA SAYAJIRAO UNIVERSITY OF BARODA, VADODARA.**

I take privilege in declaring that the work is developed, structured and prepared by me solely. Further I declare that work is not part of any declared or published work partly or fully for the award of any degree or academic qualifications of this university or of any other institutions of examining body in India and abroad.

A handwritten signature in black ink, appearing to be "Jitendra G. Jamnani".

**( JITENDRA G. JAMNANI )**

Place: Vadodara

Date: 5/11/09