

Curriculum-Vitae

Yagnesh B. Shukla

Associate Professor

Department of Electronics and Communication Engineering

Sardar Vallabhbhai Patel Institute of Technology (SVIT)

Vasad-388306

GUJARAT, INDIA

Educational Qualifications

Sr. No.	Examination passed	University	Years of passing	% of marks	Main Subjects
1	B.E.	N.M. Unvi., Jalgoan	1998	66.13%	Electronic Engg.
2	M.E.	M.S. Unvi., Baroda	2004	73.78%	Electrical Engg.

List of Publications

- [1] Y.B.Shukla, and S.K.Joshi, *Three phase induction heating system using bi directional switching transformer*, The International Journal of Electrical Engineering, Vol.11, Edition 1, pp.185-190, 2011. ISSN1582-4594
- [2] Y.B.Shukla, and S.K. Joshi, *Induction heating system using self oscillating driver*, The International Journal of Electrical Engineering, Vol.11, Edition 2 ,pp.107-112, 2011. ISSN1582-4594
- [3] Y.B.Shukla, S.K.Joshi, R.J.Makwana and A.A.Daiya, *AC to AC Voltage controller using PWM Technique without DC link*, The National Conference on recent trends in engineering and technology, NCRTEET-2011, BVM Engineering College, V.V.Nagar, 13-14 May 2011, ISBN978-81-921358-3-0
- [4] Y.B.Shukla, and S.K. Joshi, *Modulation Techniques for Three Phase IDH*, Journal of Power Electronics, accepted.
- [5] Y.B.Shukla, and S.K. Joshi, *Configuration Proposals for an Optimal Electromagnetic Coupling in IDH System for conducting Material*, IET Power Electronics, under review.