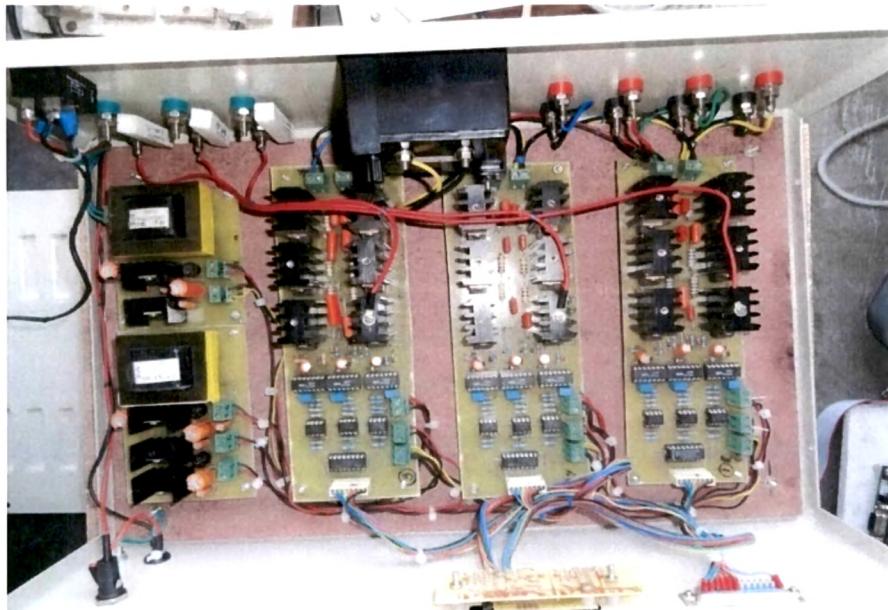


## APPENDIX B

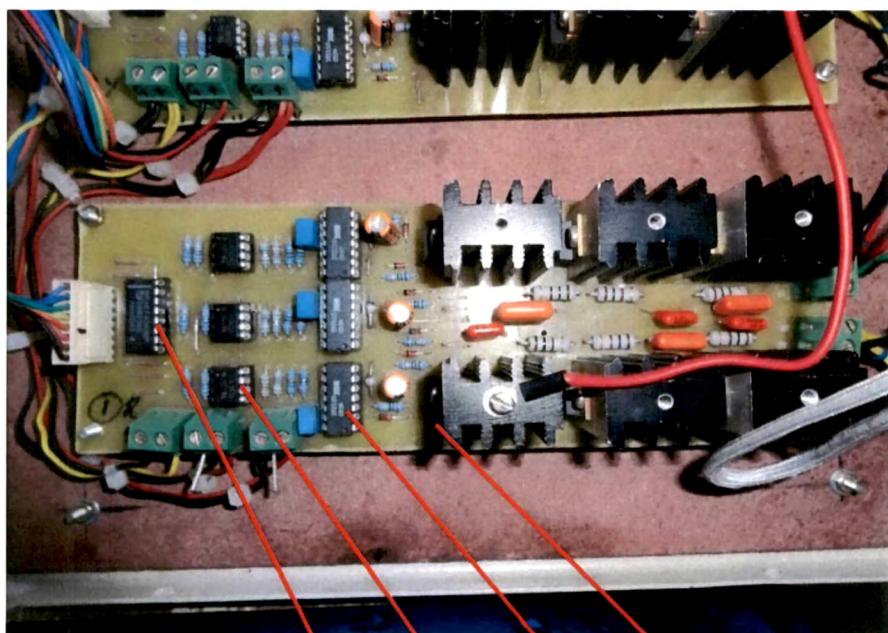
---

## PHOTO GALLERY

---



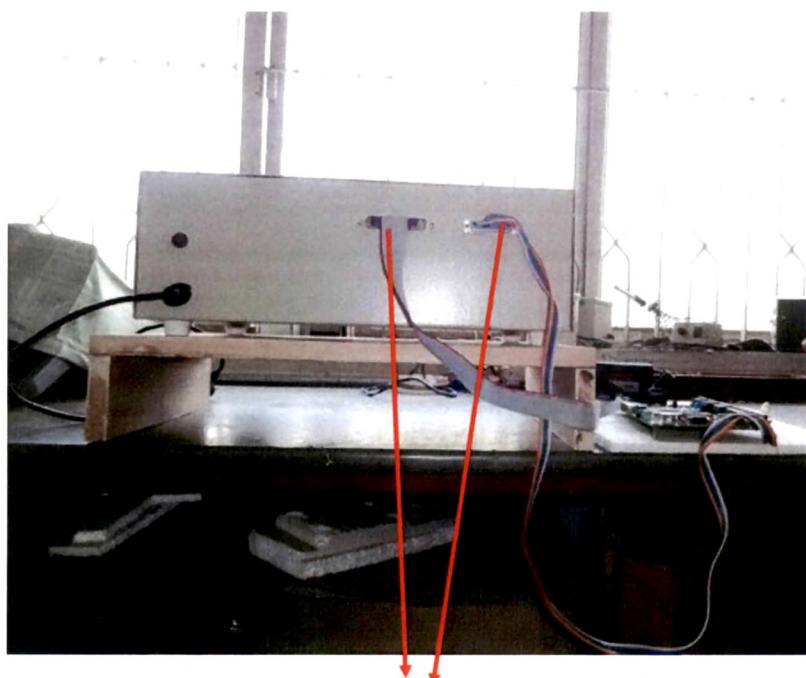
**Fig. B.1 Hardware for control and power circuit**



**Fig. B.2 Buffer, optoisolator, driver and power switch**



**Fig. B.3 Panel for control and power circuit**



**Fig. B.4 Gating signals**

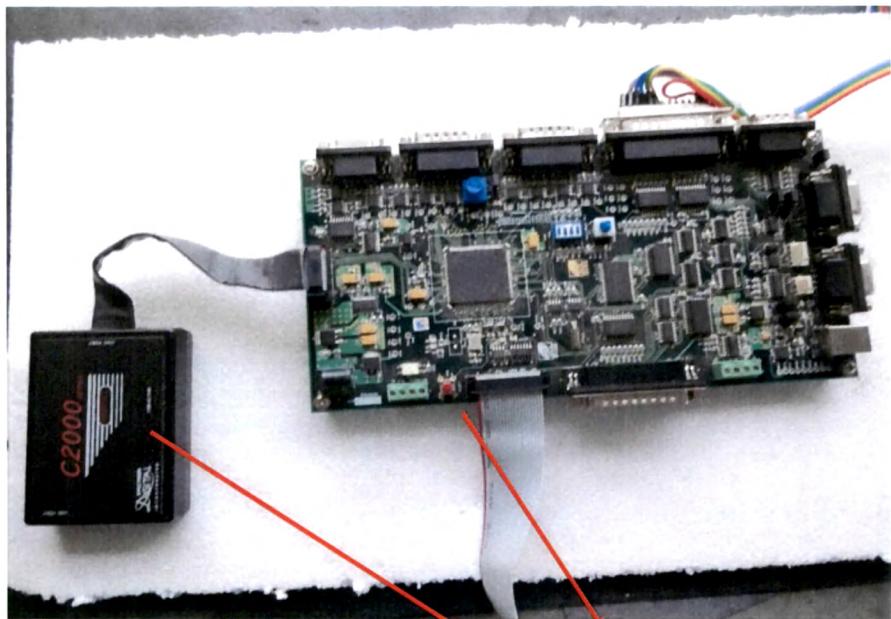


Fig. B.5 Emulator and EPB28335

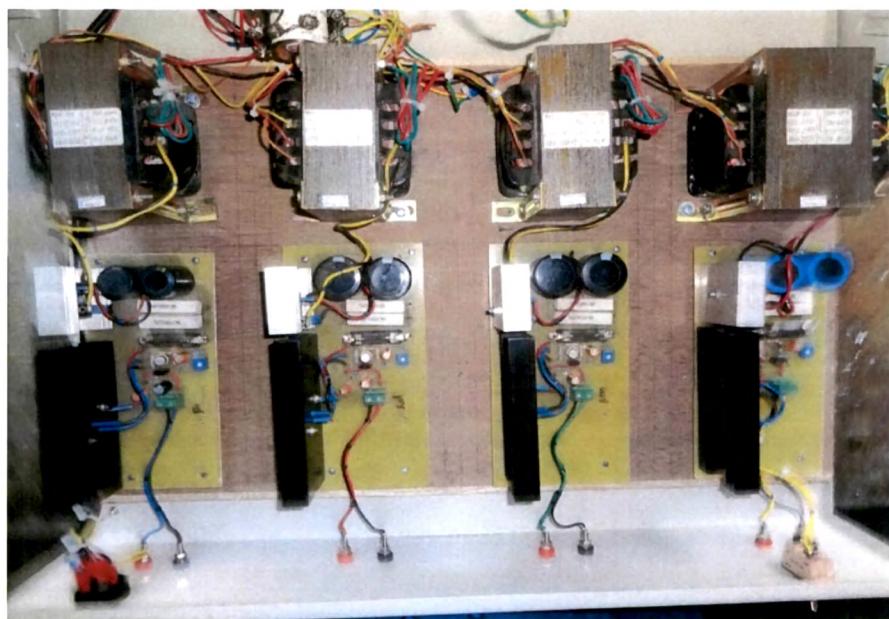
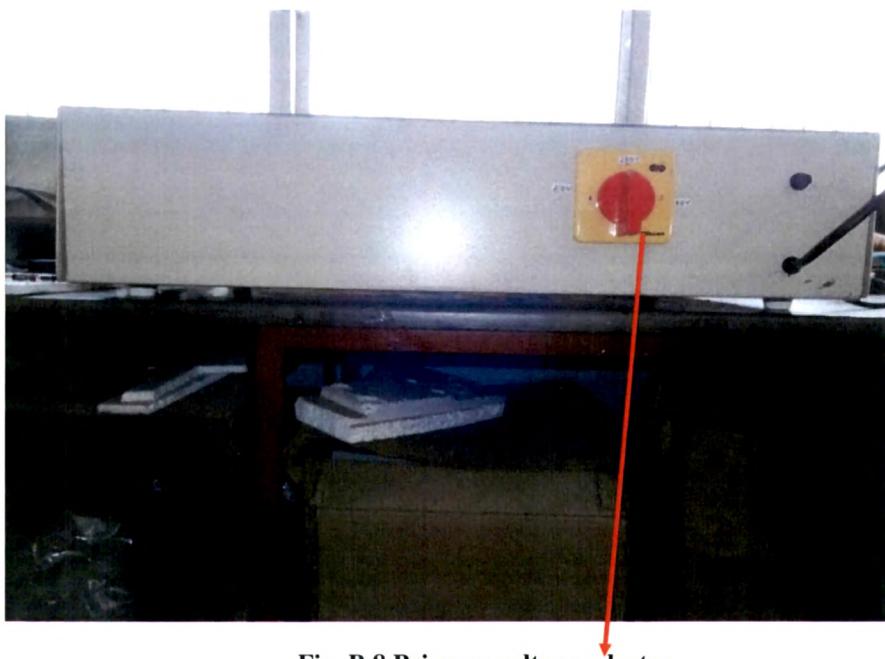


Fig. B.6 Hardware for regulated power supply



**Fig. B.7 Panel for regulated power supply**



**Fig. B.8 Primary voltage selector**

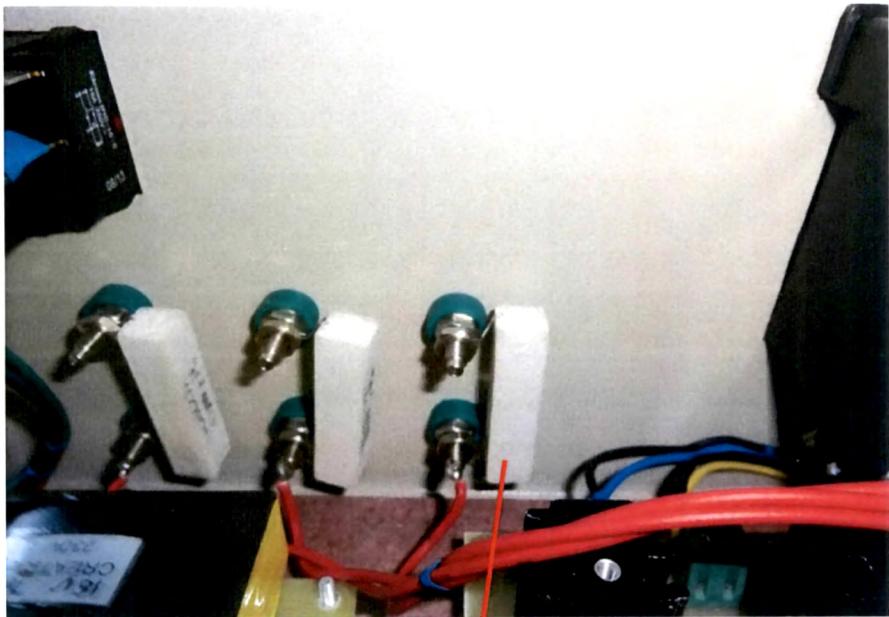


Fig. B.9 Setup for current measurement

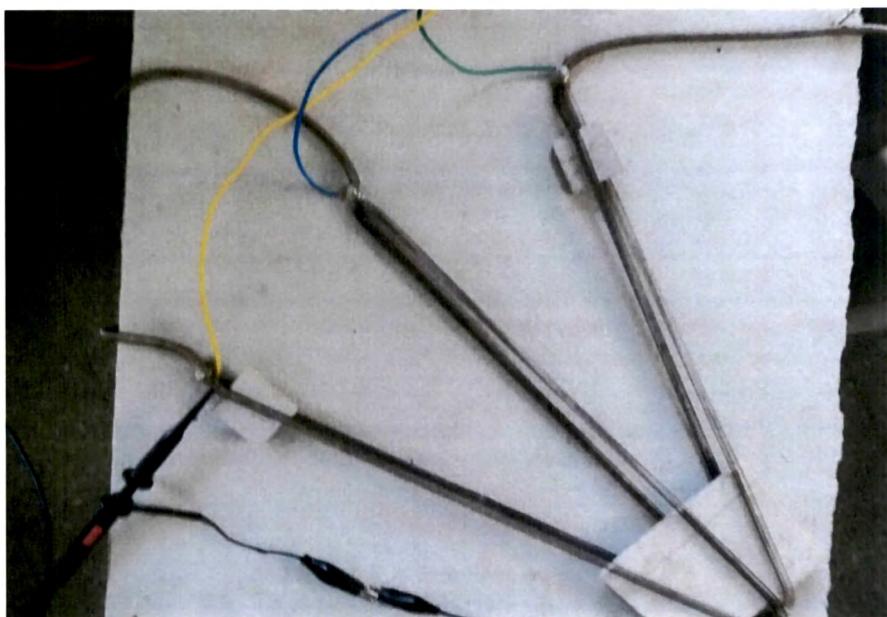


Fig. B.10 Load



Fig. B.11 System setup

```
00000000 3FF9CE 28AD0006 MOV    @SP
00000001 3FF9D0 561F SETC   OBJI
00000002 3FF9D1 5616 CLRC   AMO
00000003 3FF9D2 561A SETC   MOM
00000004 3FF9D3 2940 CLRC   PAG
00000005 3FF9D4 761F0000 MOVW   DP
00000006 3FF9D6 2902 CLRC   OVM
00000007 3FF9D7 FF69 SPM    0
00000008 3FF9D8 767FF674 LCR    0x3
00000009 3FF9DA 56CF0002 BF     2,UI
0000000A 3FF9DC 28AD0006 MOV    @SP
0000000B 3FF9DE 2BBD MOV    *SP
0000000C 3FF9DF 2BBD MOV    *SP
0000000D 3FF9E0 1EBD MOVL   *SP
0000000E 3FF9E1 0007 POP    RPC
```

Fig. B.12 Emulator connected

## Appendix B

## Photo Gallery

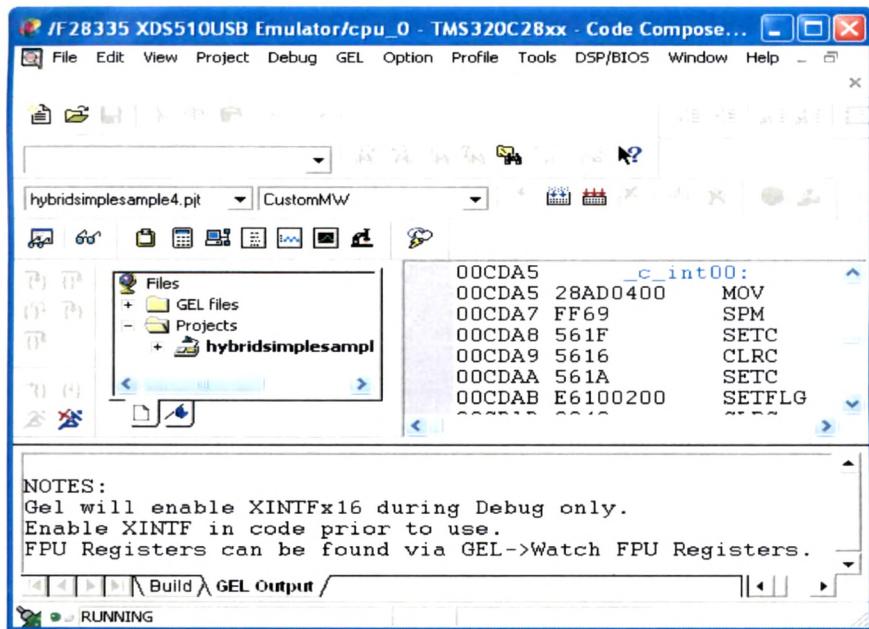


Fig. B.13 Project built