

APPENDIX B

PHOTO GALLERY

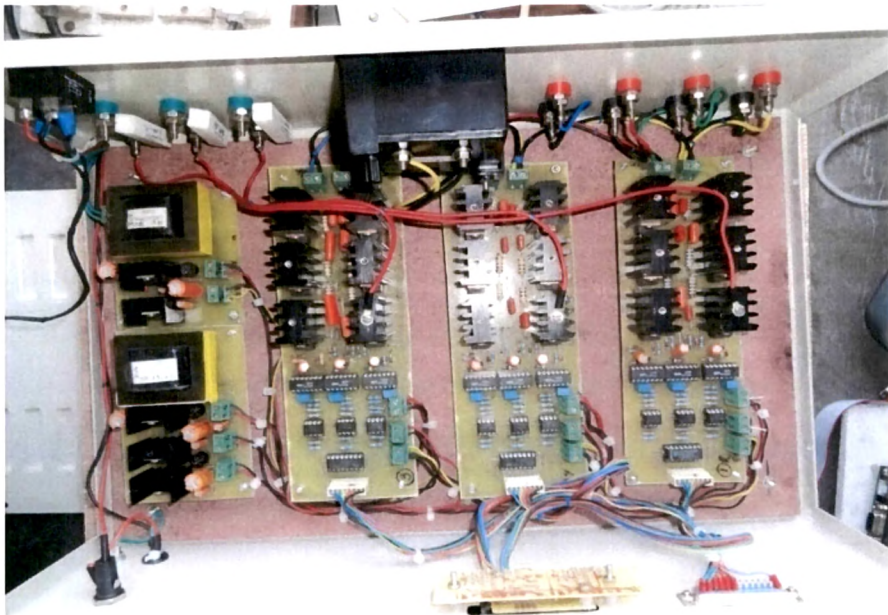


Fig. B.1 Hardware for control and power circuit

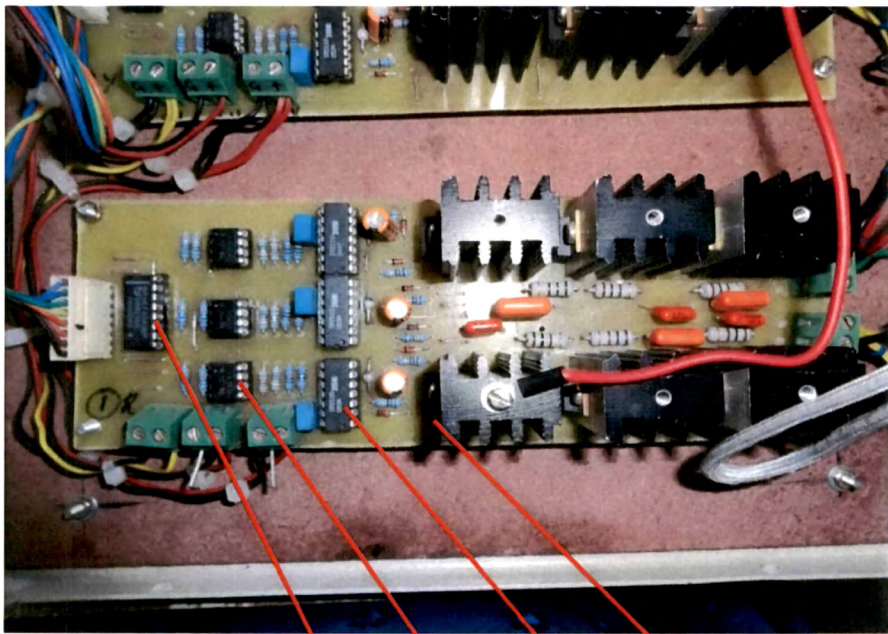


Fig. B.2 Buffer, optoisolator, driver and power switch



Fig. B.3 Panel for control and power circuit

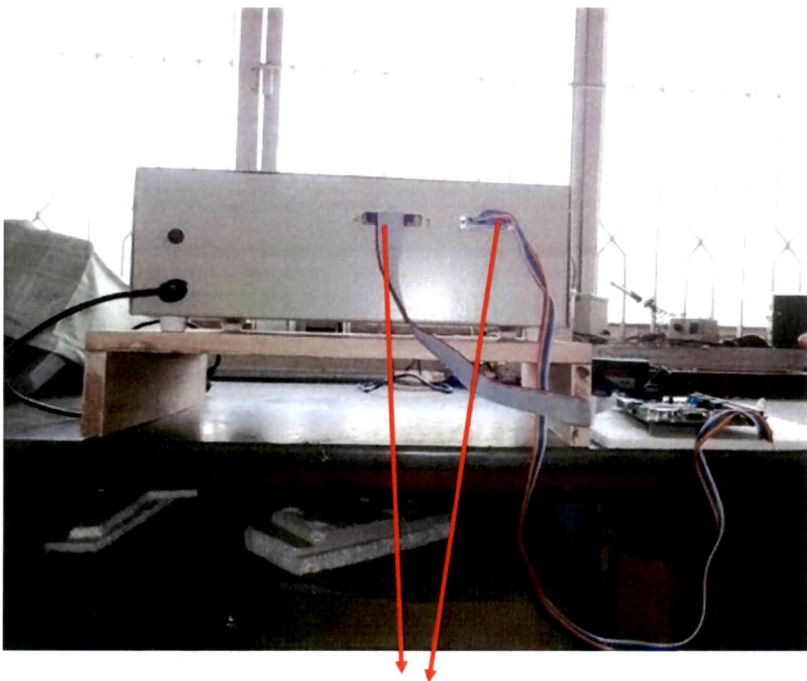


Fig. B.4 Gating signals

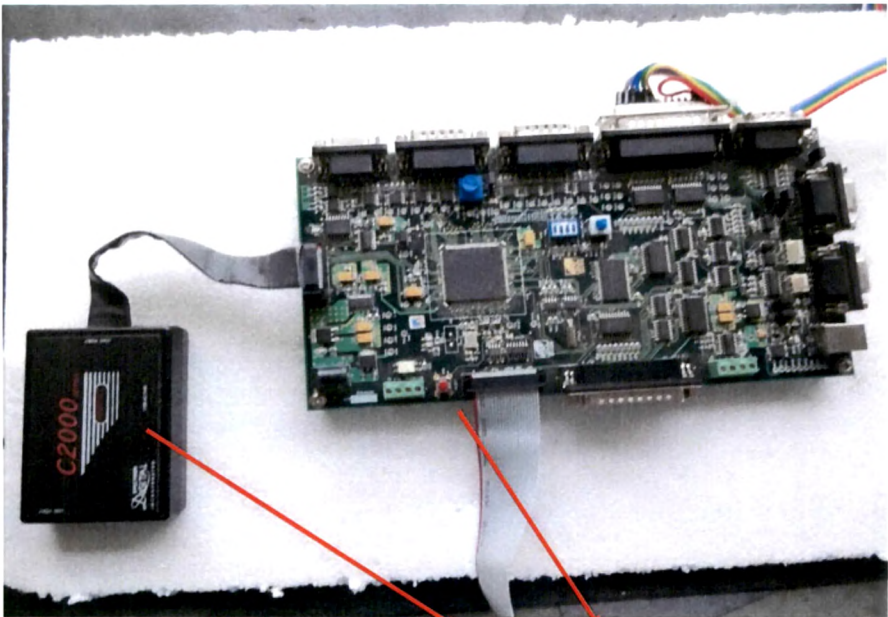


Fig. B.5 Emulator and EPB28335

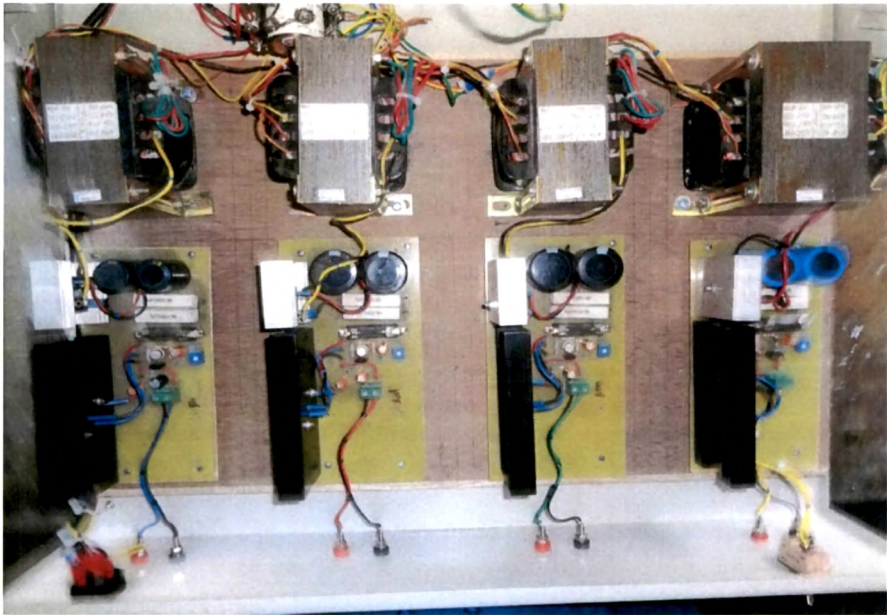


Fig. B.6 Hardware for regulated power supply



Fig. B.7 Panel for regulated power supply

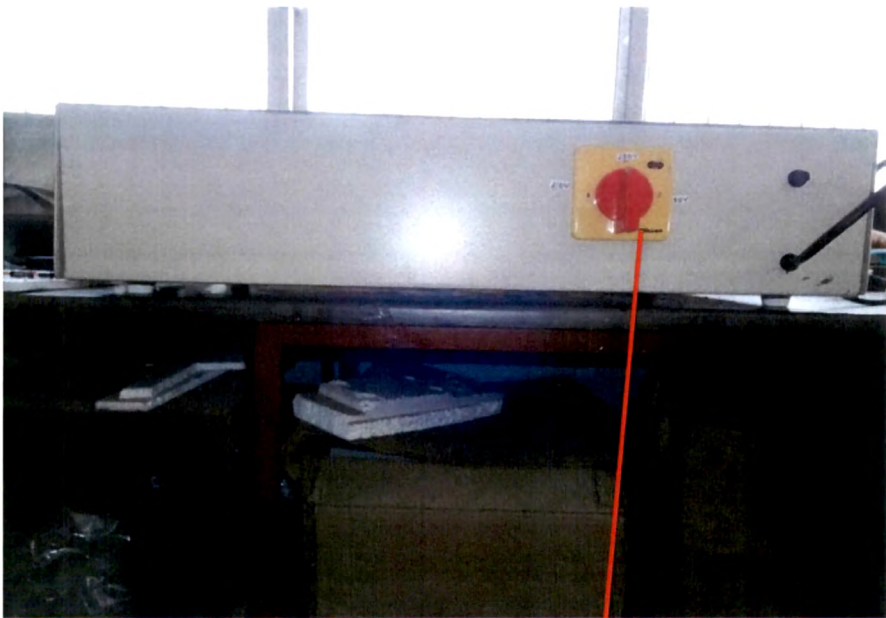


Fig. B.8 Primary voltage selector

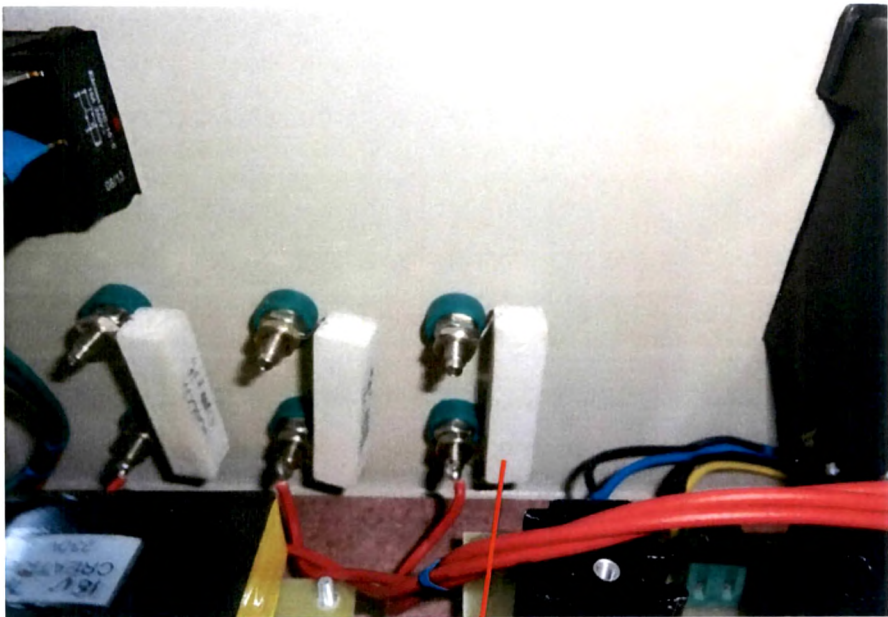


Fig. B.9 Setup for current measurement

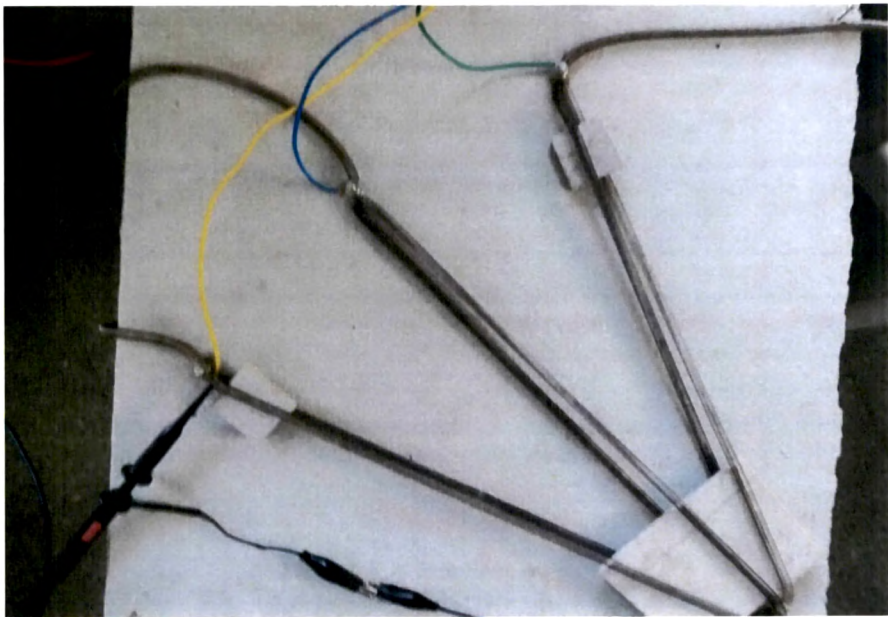


Fig. B.10 Load

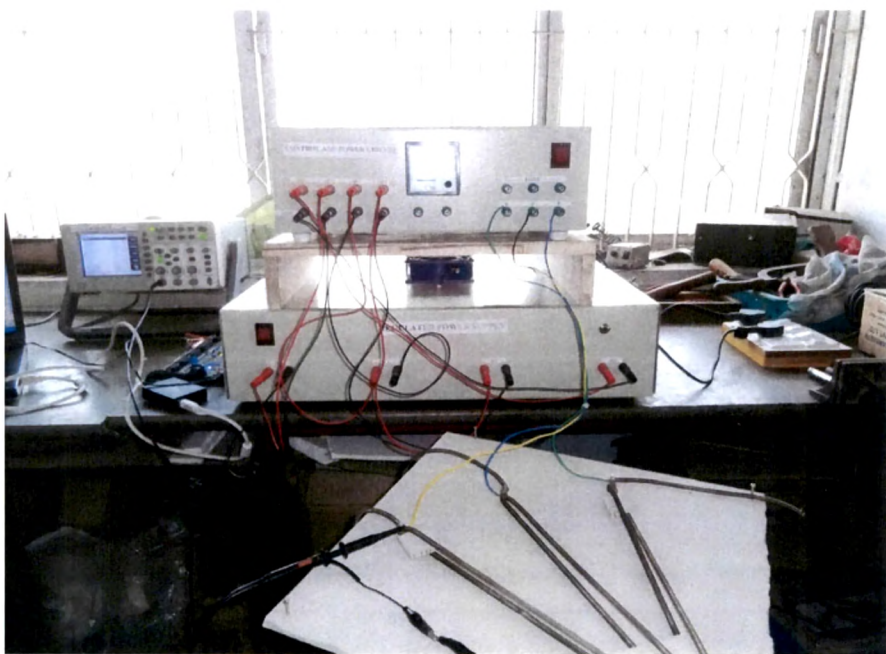


Fig. B.11 System setup

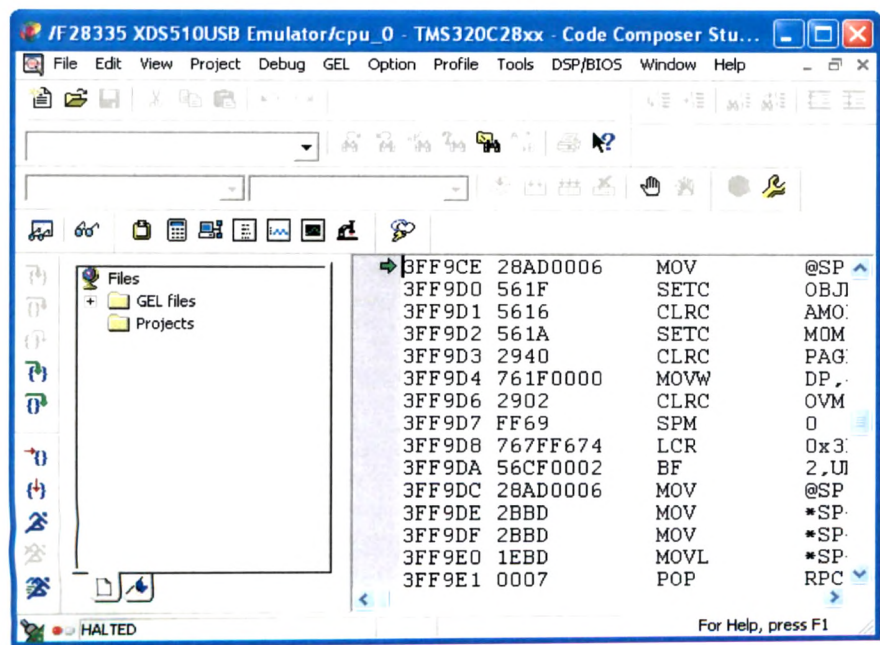


Fig. B.12 Emulator connected

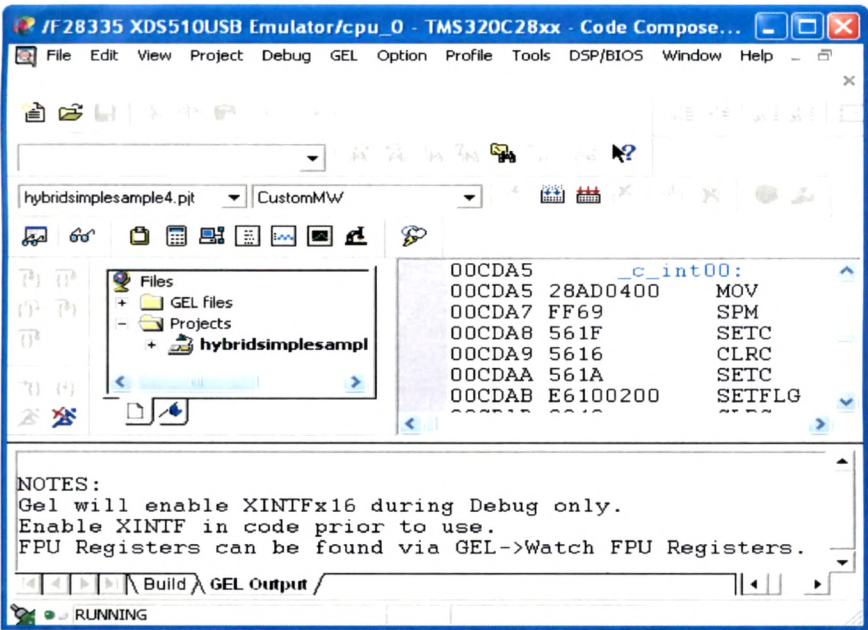


Fig. B.13 Project built