

ACKNOWLEDGEMENT

The author wishes to express a deep sense of gratitude to his guide Dr.S.M.Sen, Dean, Faculty of Technology and Engineering,Baroda for the kind help, constant inspiration and consistent encouragement he has showered during the course of this work.

The author is indebted to his following colleagues- Prof.K.K.Thakore for the valuable technical discussions and encouragement, Dr.R.C.Desai and Shri C.S.Lalwani for helping in Computer programming, Shri Y.K.Kardile and Shri M.M.Pathan for the design,development and testing of various electronic circuits,and Shri A.P.Nerurkar for linguistic editing of the manuscript.

The author also acknowledges the technical guidance and help rendered by Power System Group of the Department of Electrical Engineering,Indian Institute of Science,Bangalore at various stages of the work.

The Co-operation and assistance rendered by Shri A.S. Karkhanis in making the necessary sketches and Shri V.L.Joshi in neatly typing the manuscript have helped to a great extent in presenting this work in the present form. To them the author is sincerely grateful .
