

**Hardware Software Co-design for
Network Processor Architecture through
Integration of Scheduling Algorithm for
Switching Fabric using VLSI**

**A thesis submitted
for award of the Degree of
DOCTOR OF PHILOSOPHY
in
Electrical Engineering**

**By
Shah Mihirkumar Vinodchandra**



**DEPARTMENT OF ELECTRICAL ENGINEERING
FACULTY OF TECHNOLOGY & ENGINEERING
THE MAHARAJA SAYAJIRAO UNIVERSITY OF BARODA
VADODARA – 390 001, GUJARAT, INDIA
JULY 2007**