CERTIFICATE

This is to certify that the thesis titled, "Hardware Software Co-design for Network Processor Architecture through Integration of Scheduling Algorithm for Switching Fabric using VLSI" submitted by **Shri "SHAH MIHIRKUMAR VINODCHANDRA"** in fulfillment of the degree of **DOCTOR OF PHILOSOPHY** in Electrical Engineering Department, Faculty of Technology and Engineering, The M. S. University of Baroda, Vadodara is a bonafide record of investigations carried out by him in the Department of Electrical Engineering, Faculty of Technology and Engineering, M. S. University of Baroda, Vadodara under my guidance and supervision. In my opinion this has attained the standard fulfilling the requirements of the Ph.D. Degree as prescribed in the regulations of the University.

July, 2007

Guide :

Prof. A. I. Trivedi

Department of Electrical Engineering,
Faculty of Technology & Engineering,
The Maharaja Sayajirao University of Baroda,
Vadodara – 390 001

HEAD
Electrical Engineering Deptt.,
Faculty of Tech. &
M. S. Univ. of Baroda, VADODARA

Head:

Prof. A. I. Trivedi

Department of Electrical Engineering,
Faculty of Technology & Engineering,
The Maharaja Sayajirao University of Baroda,
Vadodara – 390 001

HEAD
Electrical Engineering Deptt.,
Faculty of Tech. &
M. S. Univ. of Baroda, VADODARA

Dean :

Faculty of Technology & Engineering,
The Maharaja Sayajirao University of Baroda,
Vadodara – 390 001

DEAN
Faculty of Tech. & Engg.
M. S. University of Baroda,
VADODARA.