

LIST OF FIGURES

2.1(a)	Six-Phase configuration	24
2.1(b)	Twelve-Phase configuration	24
2.2	954 Kcmil (RAIL) ACSR Conductor	43
2.3	GETCO'S Existing 400 KV 3-ph Double Circuit , 10 inch Bundle Spacing Twin Mouse Conductor Used, Dia. 31.77 MM, 900A C.C./Conductor	44
3.1	Schematic representation of a 3-ph / N-ph Transformer	58
3.2	Wye / Star Connection	62
3.3	Equivalent Circuit of Wye / Star Transformer	62
3.4	Equivalent diagrams of different 3-phase/6-phase Transformer schemes	65
3.5	Schematic Diagram of Multi-phase Line Connected via 3-ph / N-ph Transformers to 3-ph Network	68
3.6	Schematic Diagram of 3-phase Line Connected via N-ph / 3-ph Transformers to Multi-phase Network	70
3.7	A Multi-phase Load Connected to 3-phase bus bar	71
3.8	A three-phase Load Connected to N-phase bus bar	71
3.9	Charging Current at S.E. V/S Line Length	74
3.10	Voltage Gain for (O.C. at R.E.) Line Represented by Equivalent π - Circuit	75
4.1	A Circuit model for line loadability study	79
4.2	Flowchart of central solution loop	82
4.3	A Line model for loadability dependence on Var supply	83
4.4	Generalized loadability curves for 400 KV alternative lines.	85

4.5	Loadability curves for 400 KV alternative lines.	86
4.6	Effect of Shunt Compensation on Line Loadability	88
4.7	Effect of Series Compensation on GETCO'S three-phase Double Circuit Line Loadability	89
4.8	Effect of Series Compensation on (GETCO'S three-phase Double Circuit Line Converted in to Six-phase) Six-phase Line Loadability	90
4.9	Effect of Voltage drop Limit on Line Loadability	91
4.10	Effect of Variation of Steady State Stability Margin, on Line Loadability for 3-phase and 6-phase Trans. Systems.	92
4.11	Quantitative benefits in line loadability with multi-phase system	93
4.12	Thermal Duty Curves	95
4.13	Bus Voltage Curves for GETCO's 400KV three-phase Double Circuit Line and Its Conversion to Six-phase	96
4.14	Angular Displacement Curves GETCO's 400KV three-phase Double Circuit Line and Its Conversion to Six-phase	96
5.1	Effect of Capacitor Location on Constant $ A_0 $	103
5.2(a)	Effect of Capacitor Location on Constant $ B_0 $	104
5.2(b)	Effect of Capacitor Location on Angle of Constant $ B_0 $	105
5.2(c)	Effect of Capacitor Location on Angle of Constant $ B_0 $ for the 40% Series Compensated Line	105
5.3 (a,b,c)	Schematic Capacitor Location and Shunt Compensation	108
5.4	Effect of Capacitor Location and Line Length on Compensation Efficiency when Single Capacitor Bank Used	109
5.5	Effect of Shunt Compensation on 40% Series compensated line	112
5.6	Effect of Series and Shunt Compensation on Transmission Line Capacity, keeping Capacitor Bank at End of the Line	114
5.7	Effect of Series and Shunt Compensation on Trans. Line Capacity, keeping Capacitor Bank at Mid-point of the Line	115
5.8	Effect of Shunt Compensation on Ferranti over voltage	116

6.1	A Wye / Star transformer of turns ratio $(1 + t_p) : 1$	121
6.2	A multi-phase (six-phase) element connected to three-phase bus bar	124
6.3	Schematic diagram of a multi-phase (6-phase) trans. Line	126
6.4	Sample System having Three Phase Single and Double Circuits With Loads and Generations in MW	132
7.1	Detailed Connection Diagram of the Three-phase Double Circuit Simulation	138
7.2	Connection Diagram and Details of Transformers Used at the Sending End	139
7.3	Connection Diagram & Details of Transformers Used at the Receiving End	140
7.4	Details of the Distributed Line Block parameters used in the Simulation	141
7.5	Internal Details of Multi-meter used in the Simulation	142
7.6	Details of Mux Used in the Simulation	143
7.7	Internal Diagram for the sub-system:4 used for the Voltage Measurement	143
7.8	Details of the Load Sub-system and details of Load on each phase	144
7.9	Six-phase System Simulation overall view	145
7.10	Details of Generator Parameters	146
7.11	Connection Diagram and Details of the Three-phase Twelve Terminal Transformers Used for the conversion of Three-phase to Six-phase at S.E.	147
7.12	Connection Diagram and Details of the Three-phase Twelve Terminal Transformers Used for the conversion of Six-phase to Three-phase at R.E.	147
7.13	Internal Diagram of Subsystem: 3 for Line Voltage Measurement	148
7.14	Internal Diagram of Subsystem: 4 for Phase Voltage Measurement	148